

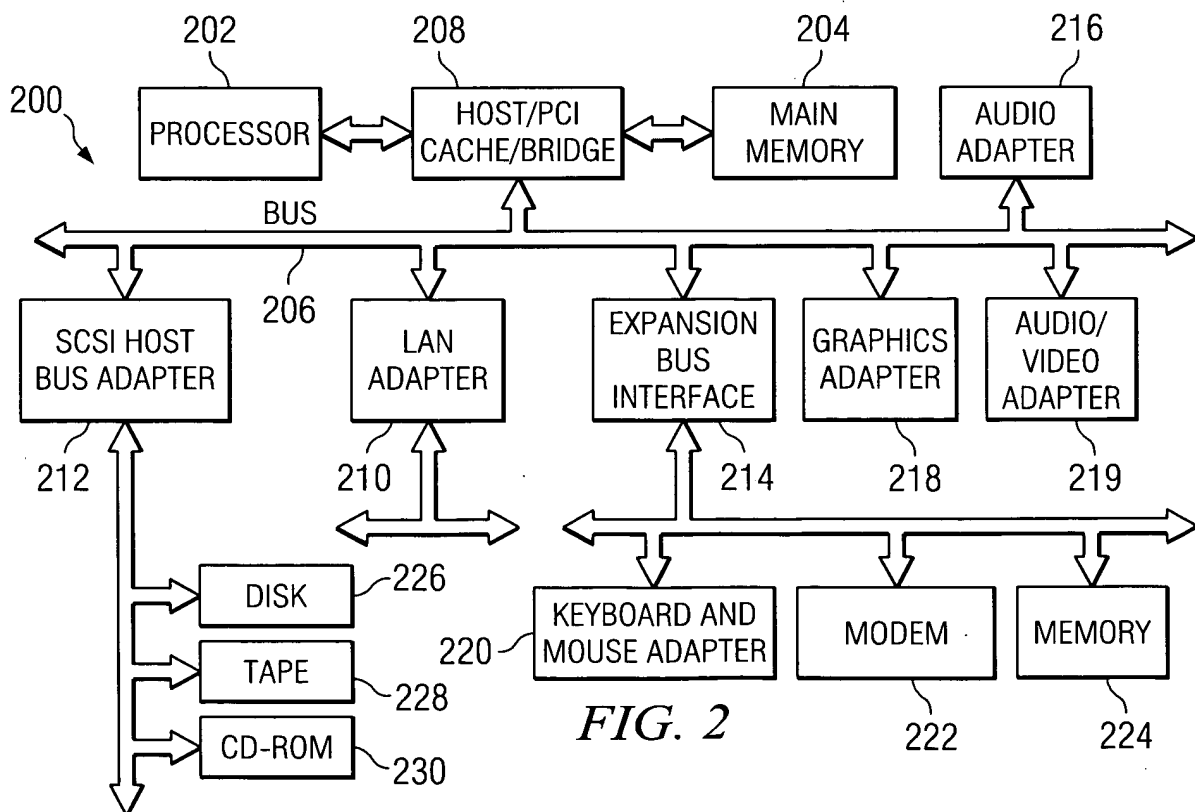
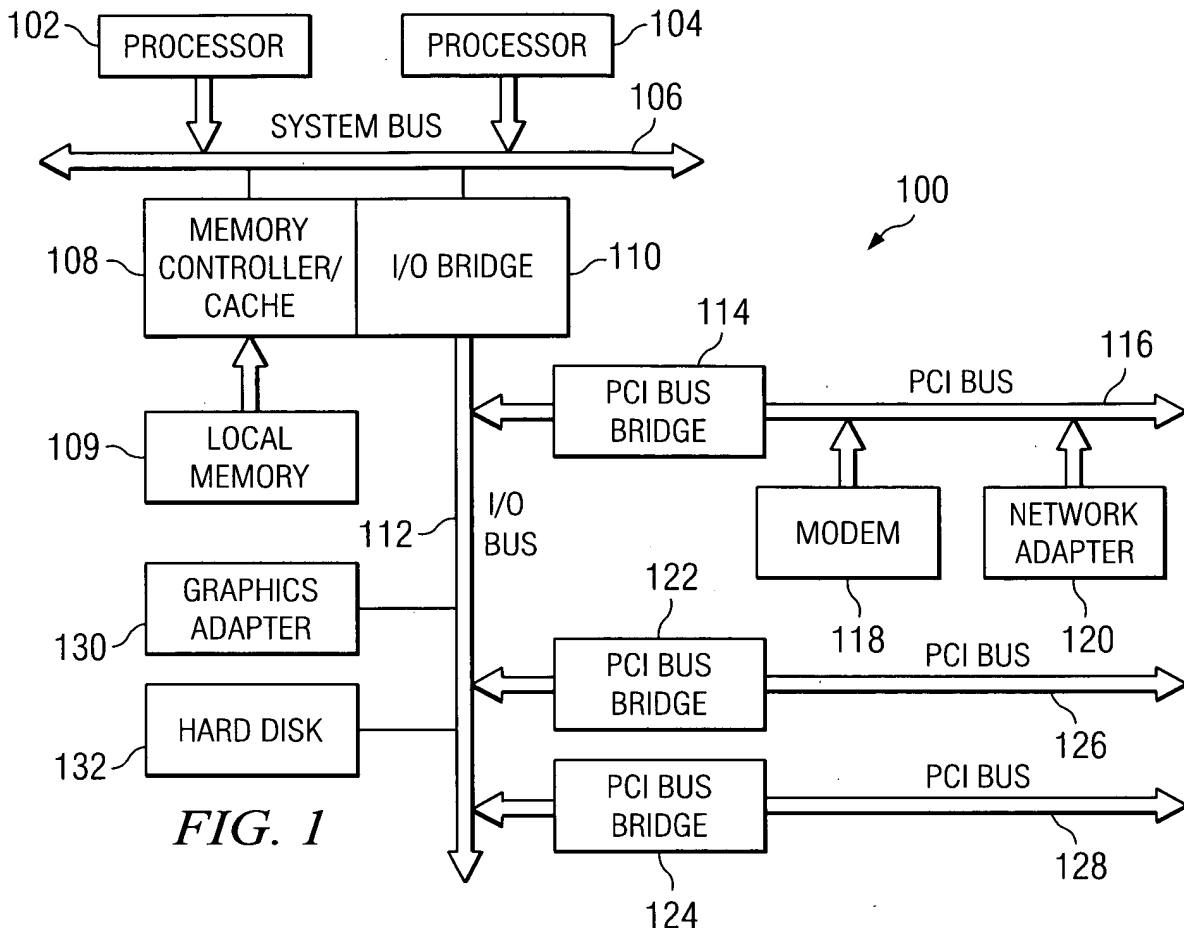


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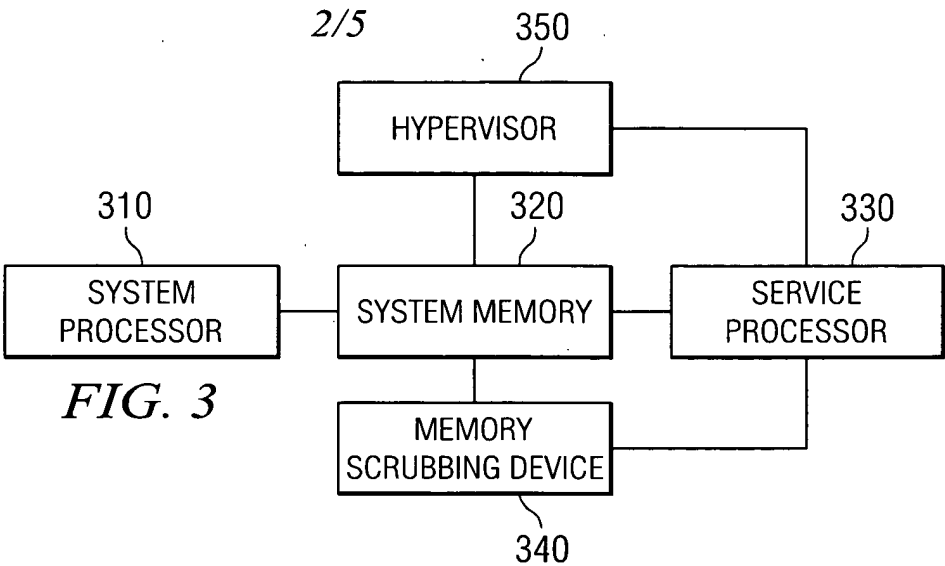


FIG. 3

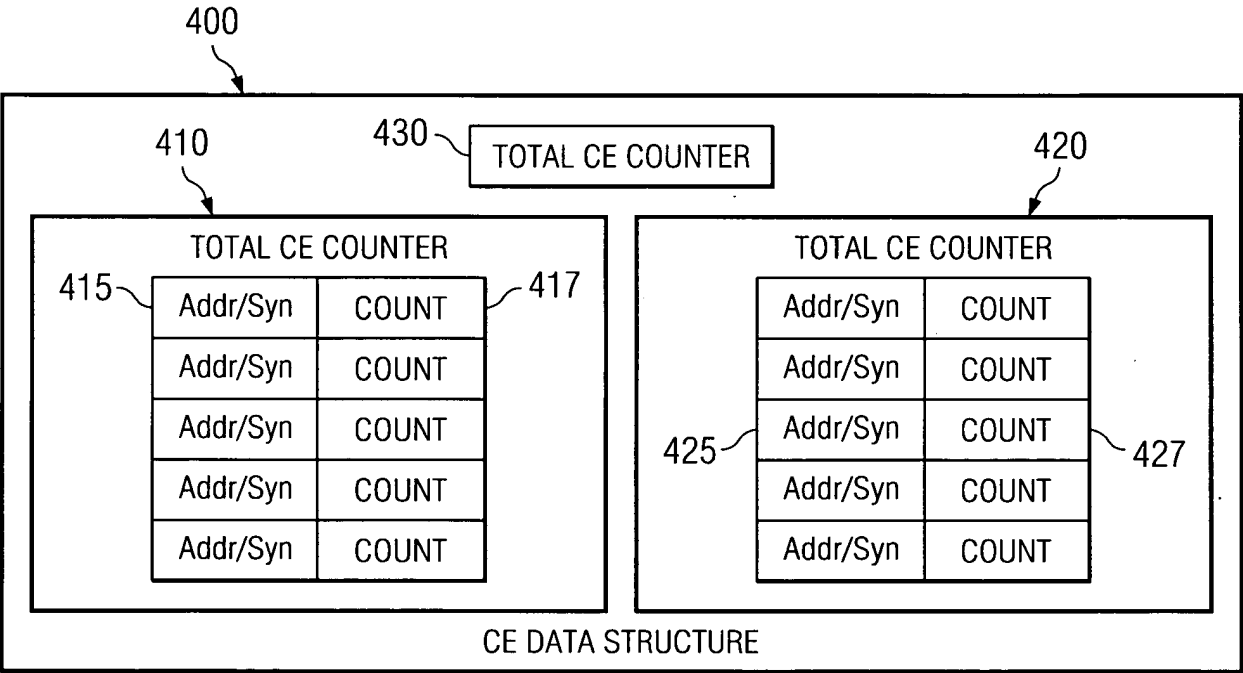


FIG. 4A

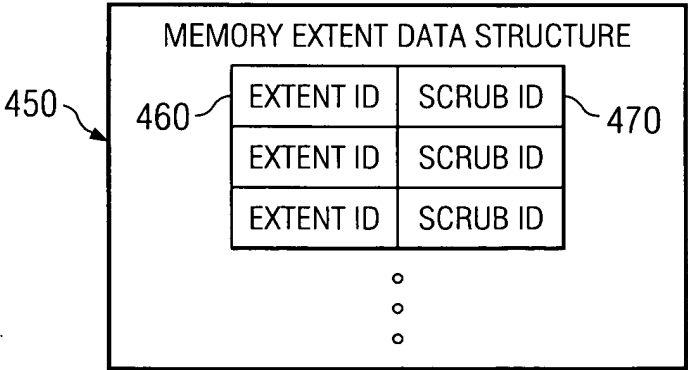


FIG. 4B



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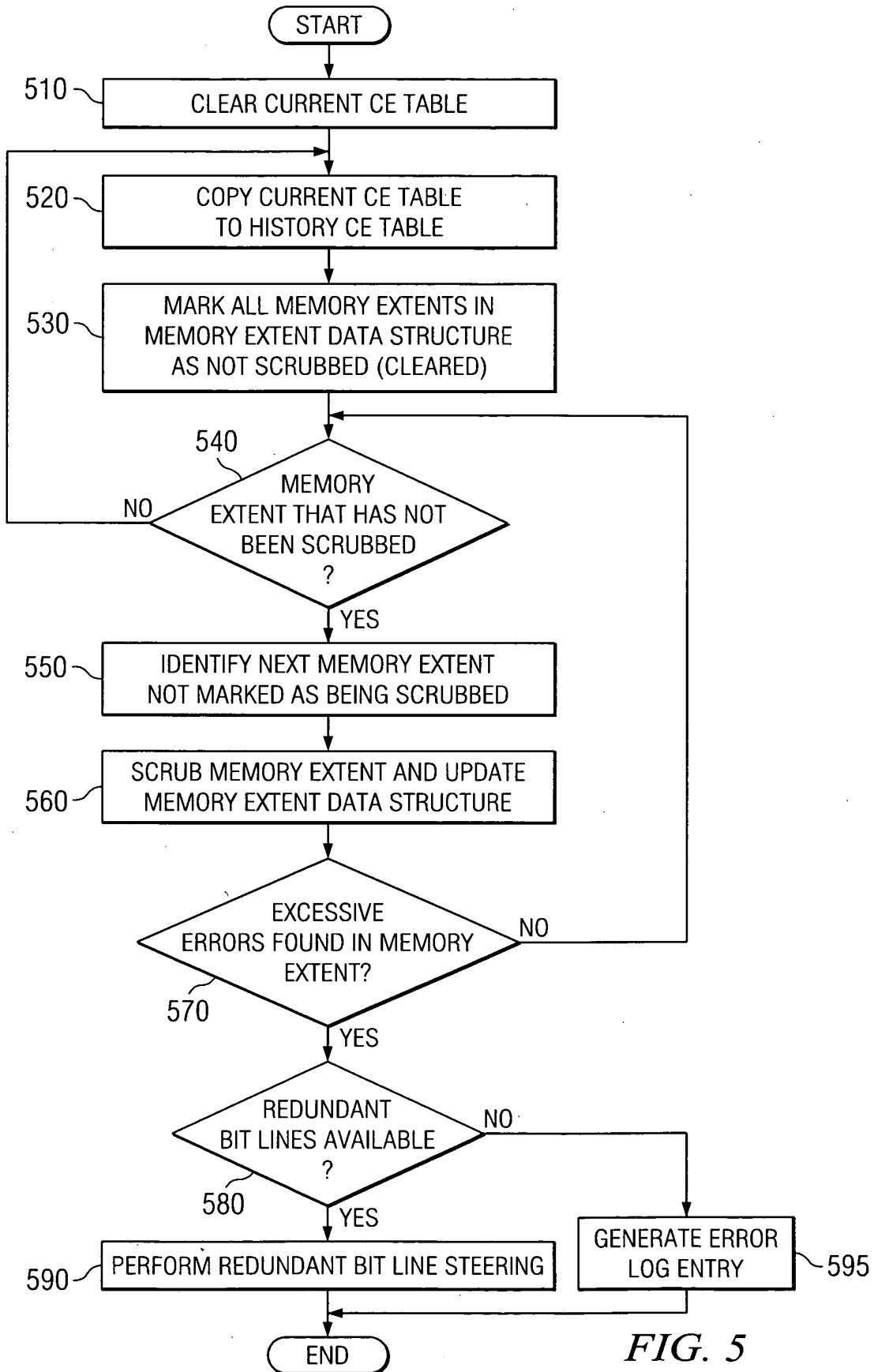


FIG. 5

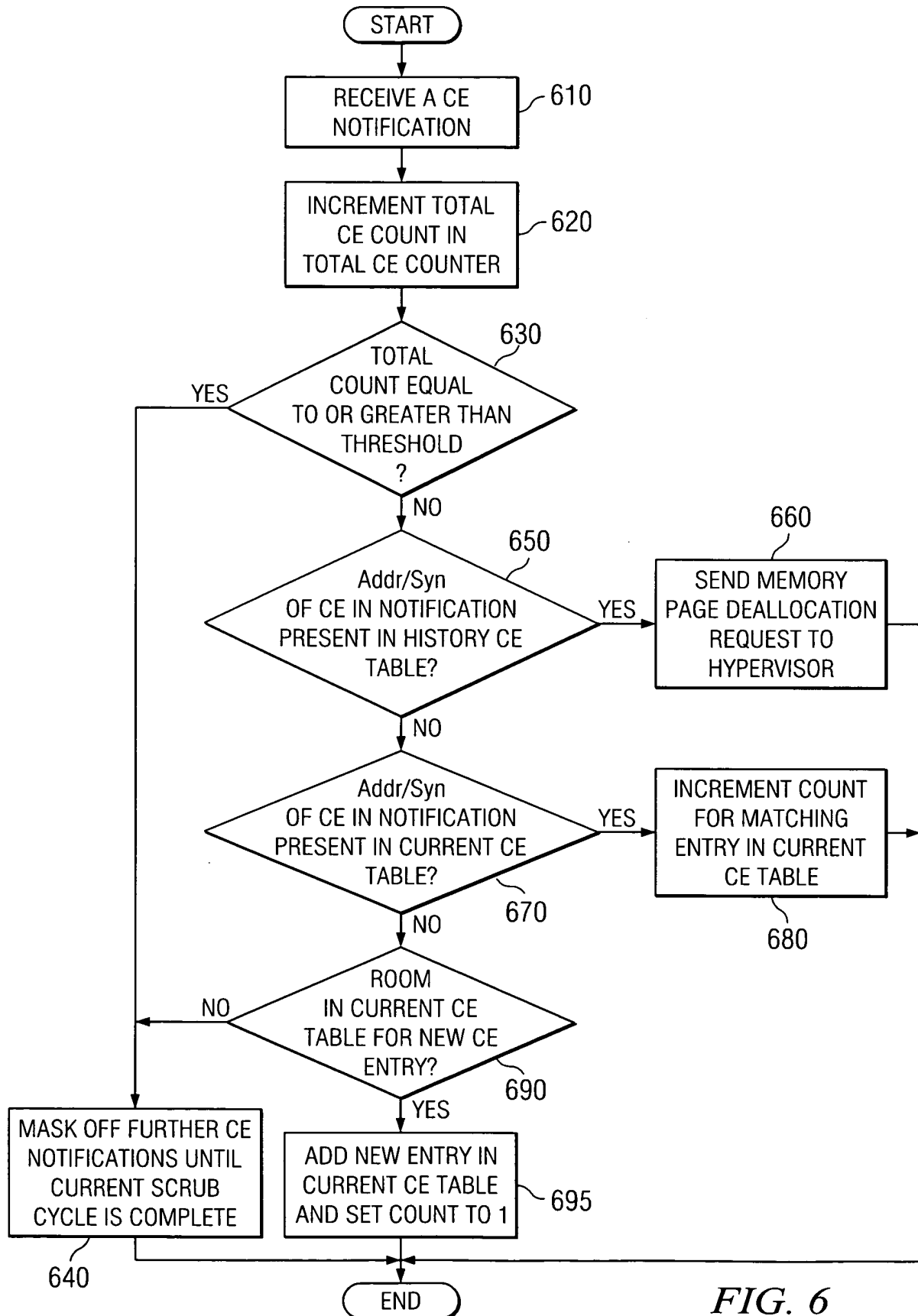


FIG. 6



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